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VadaTech Announces an FMC with Dual zSFP+/SFP28 (protocol agnostic)

Henderson, NV – September 23, 2024 – VadaTech, a leading manufacturer of integrated systems, embedded boards, enabling software and application-ready platforms, announces the [FMC121](#). The FMC121 is an FPGA Mezzanine Card (FMC) compatible with VITA 57.1 FMC carriers. It has two zSFP+/SFP28 cages which allows for Dual optics to be routed to DP0+/- and DP4+/- pins.

The FMC121 is protocol-agnostic and has a low jitter fractional PLL which can lock to CLK2 and CLK3 coming from the Carrier or be free running. The fractional PLL on the board can generate two separate clocks to the two GBT clock pins and can provide two more additional clocks on CLK0 and CLK1 for the carrier. Further the PLL could also synchronize to an external clock source via its front panel SSMC. Please consult with VadaTech Sales to discuss specific ordering options.

About VadaTech

[VadaTech](#) provides innovative embedded computing solutions from board-level products, chassis-level platforms, to configurable application-ready systems. With a focus on AdvancedTCA, MicroTCA, VPX and PCIe solutions, the company offers unmatched product selection and expertise. A unique combination of electrical, mechanical, software, and system-level expertise, enables VadaTech to provide customized commercial or rugged computing solutions to meet the most complex customer requirements. VadaTech also offers specialized product solutions for VME, CompactPCI, and other architectures. A member of PICMG and VITA, VadaTech has headquarters, design and manufacturing facilities in Henderson, NV with design, support and sales offices in Europe and Asia Pacific.

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